

Model Name: GA-G41MT-S2PT

Revision 2.12

SHEET TITLE

SHEET TITLE

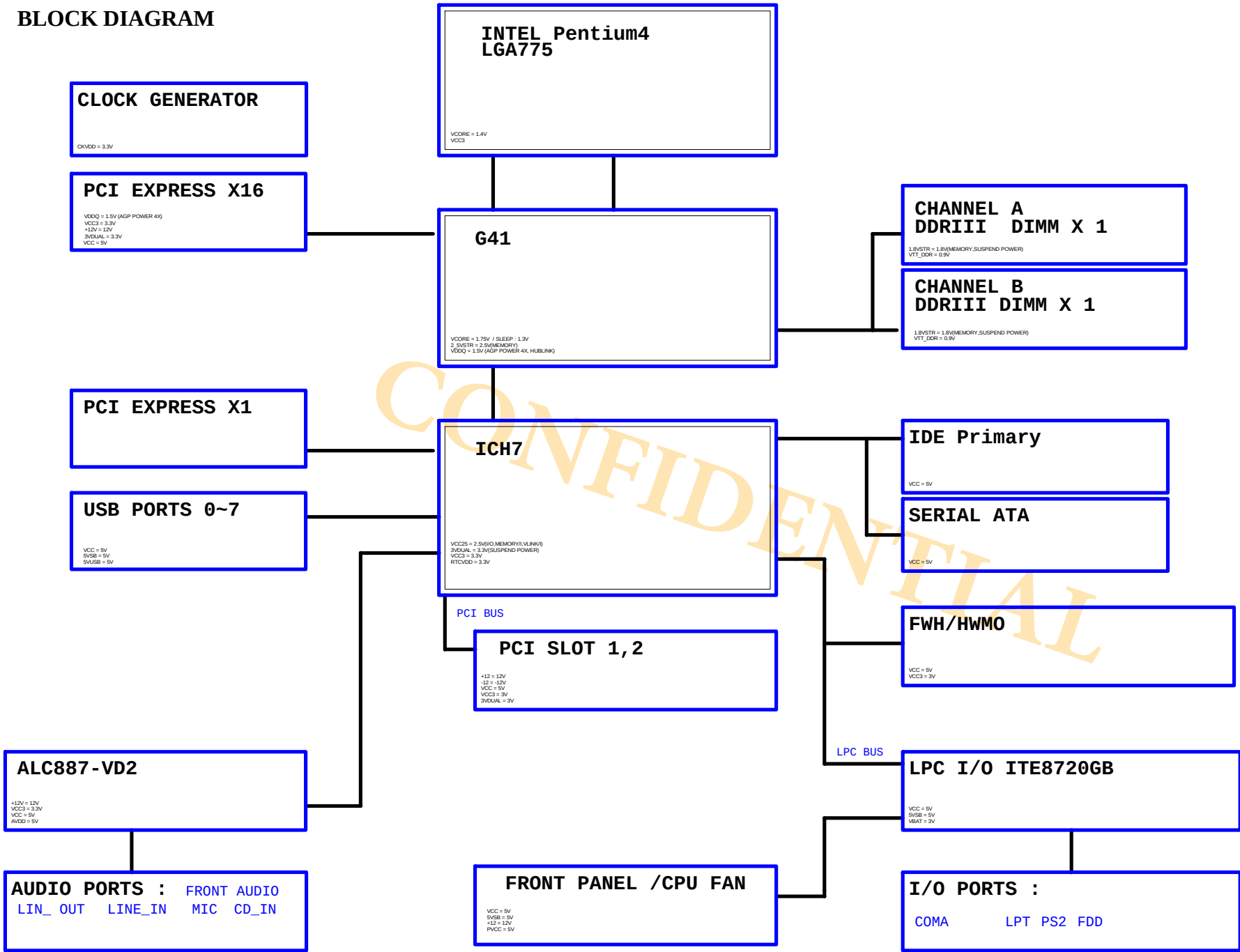
01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4_LGA775_A
05	P4_LGA775_B,D
06	P4_LGA775_C
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09	G41_DDRII
10	G41_PCI E, DMI
11	G41_VGA
12	G41_GND
13	G41_PWR
14	PCI EXPRESS*16 SLOT
15	DDR3 CHANNEL A
16	DDR3 CHANNEL B
17	DDR3 TERMINATION
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
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22	PCI SLOT 1,2,PCIE*1
23	IDE
24	ITE 8720 GB
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Gigabyte Technology		
Cover Sheet		
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BLOCK DIAGRAM



Version: 2.12

Component value change history

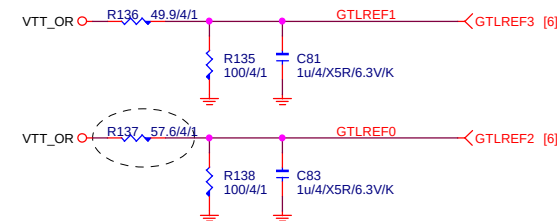
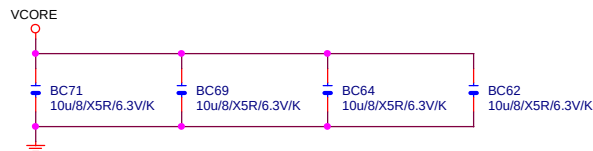
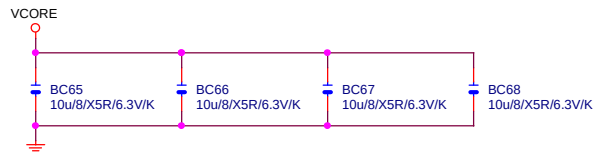
2012/05/29

[illegible]

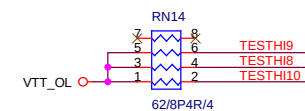
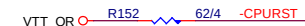
Circuit or PCB layout change for next version

[illegible]

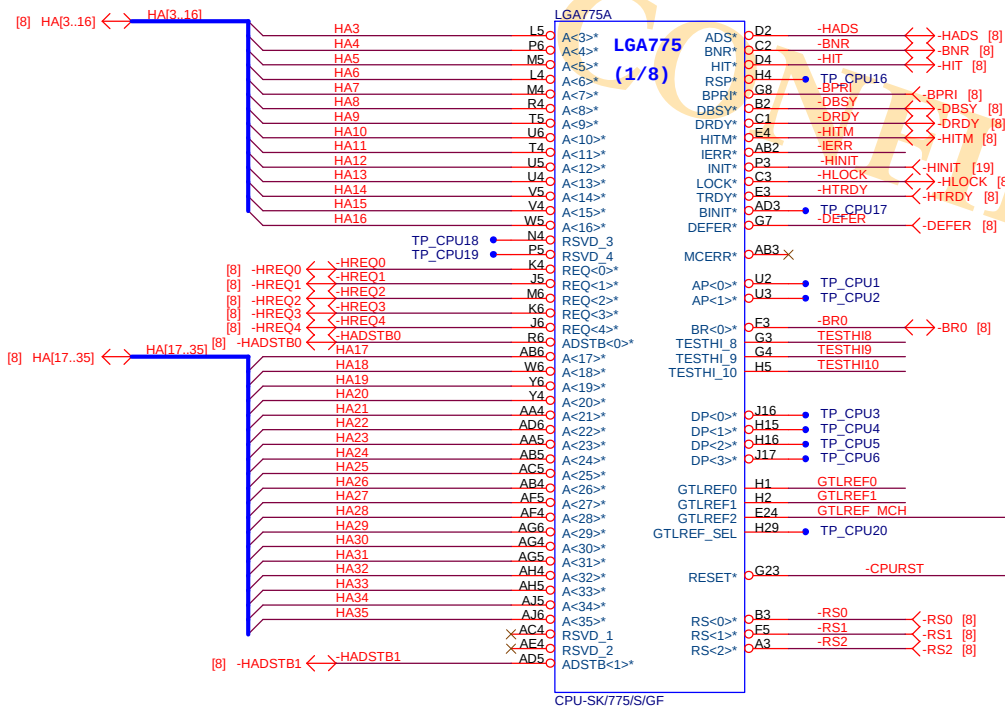
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中間値0.9V



LGA775-39-2

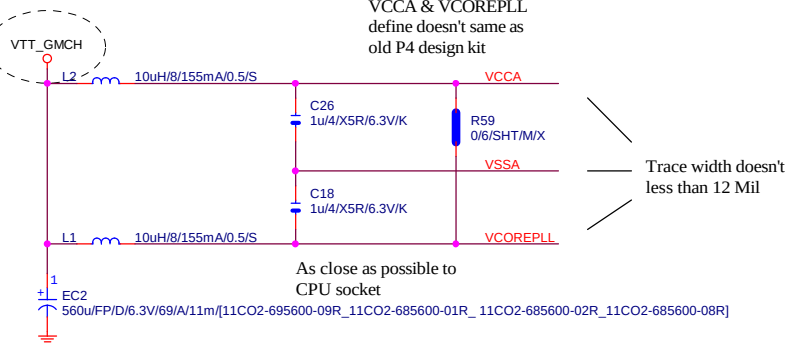


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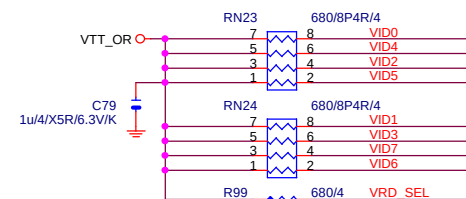
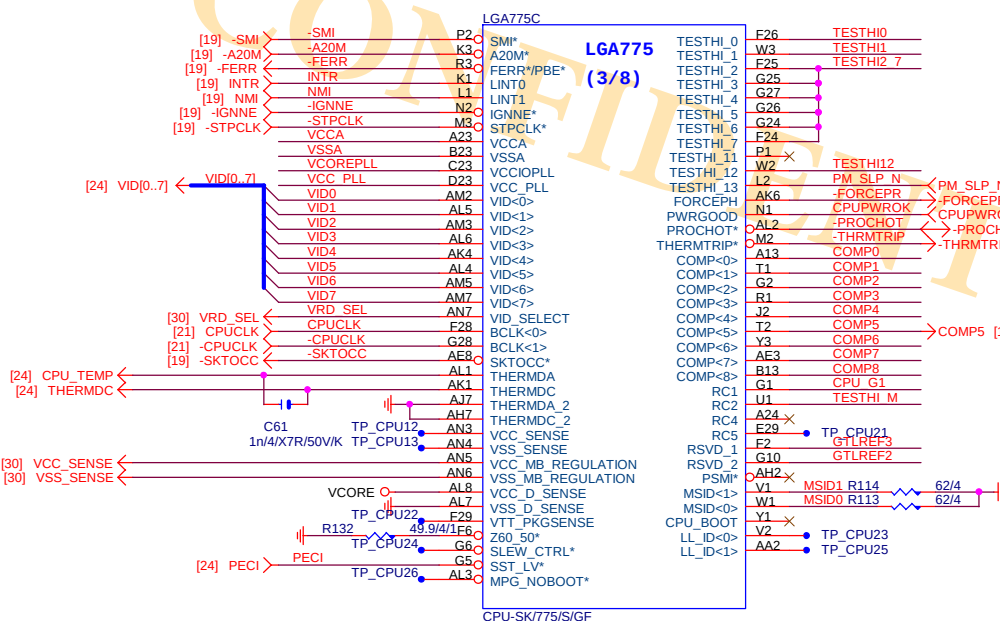
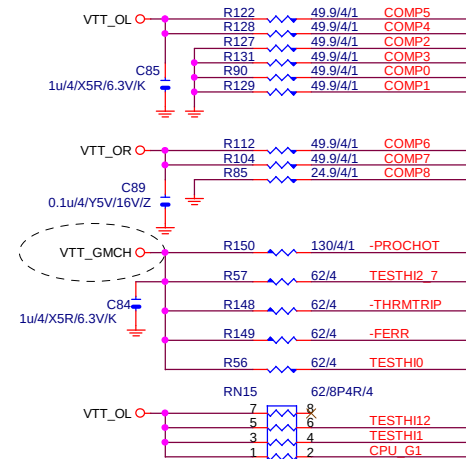
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Title P4_LGA775-A			
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Note:

VCCA & VCOREPLL
define doesn't same as
old P4 design kit

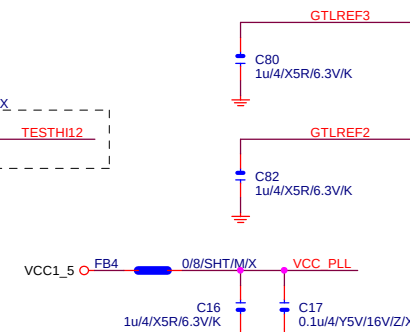


Place outside of CPU socket



GTLREF3 → GTLREF3 [4]

GTLREF2 → GTLREF2 [4]



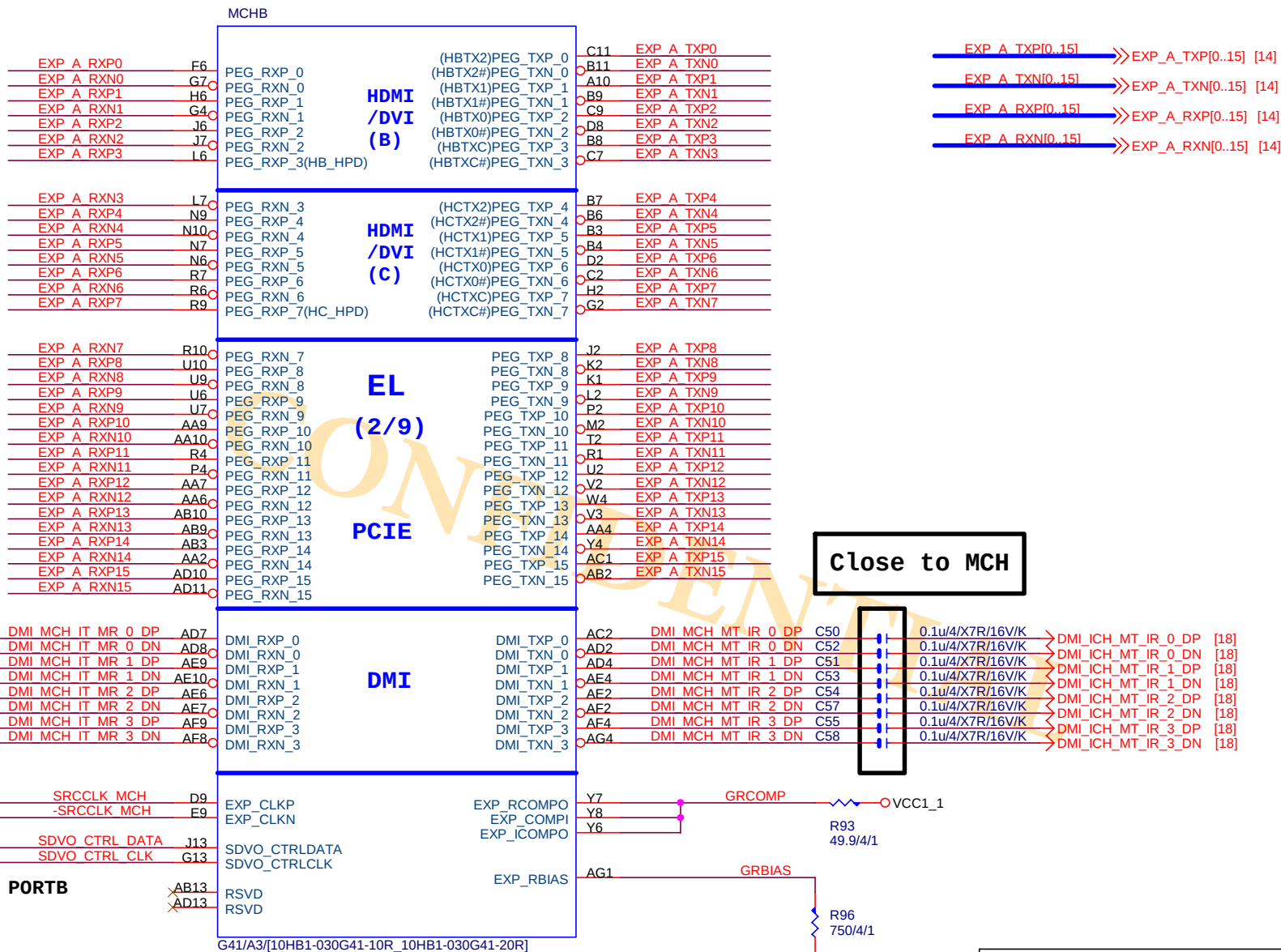
Gigabyte Technology

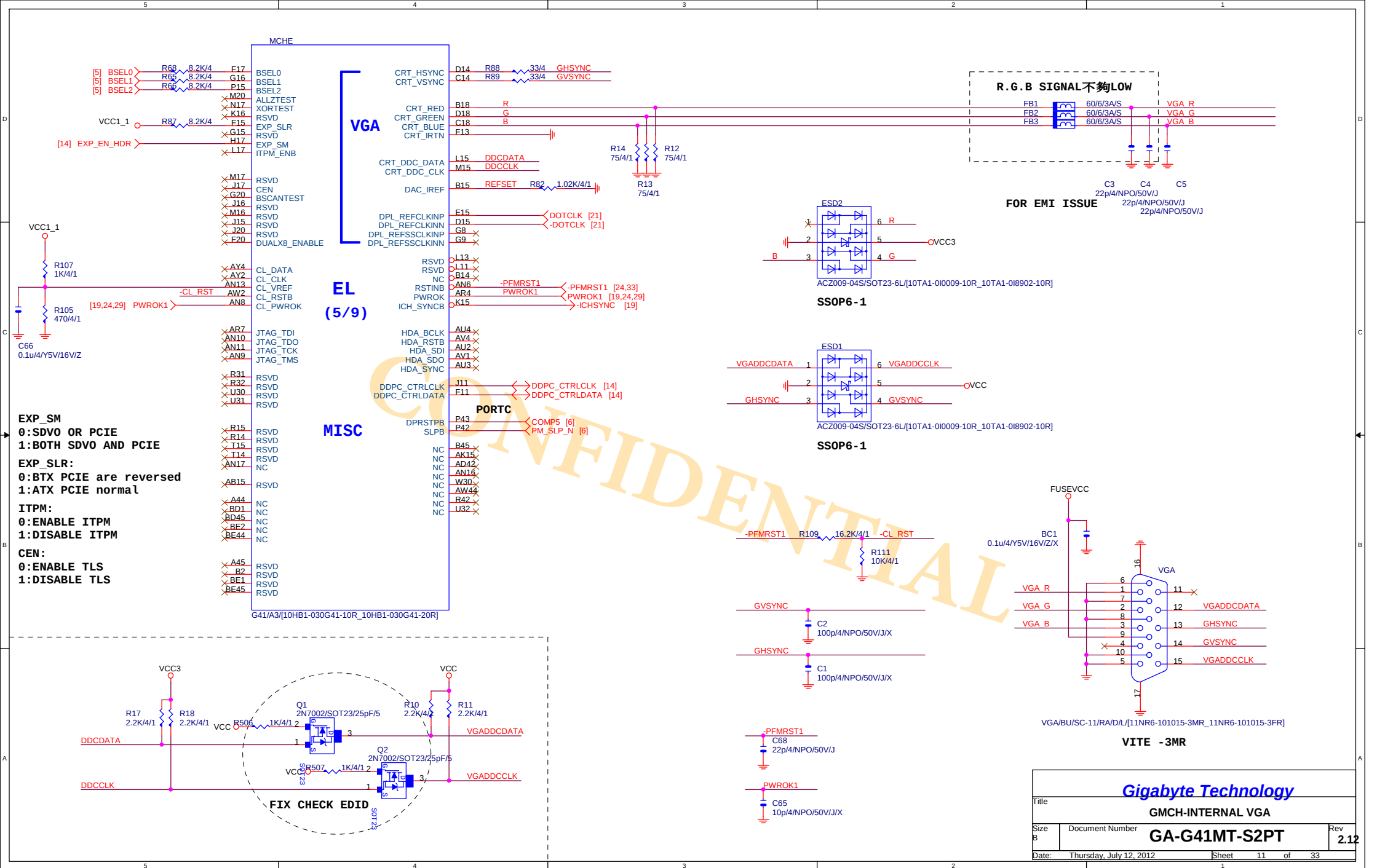
P4_LGA775-C

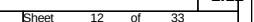
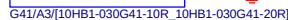
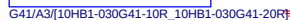
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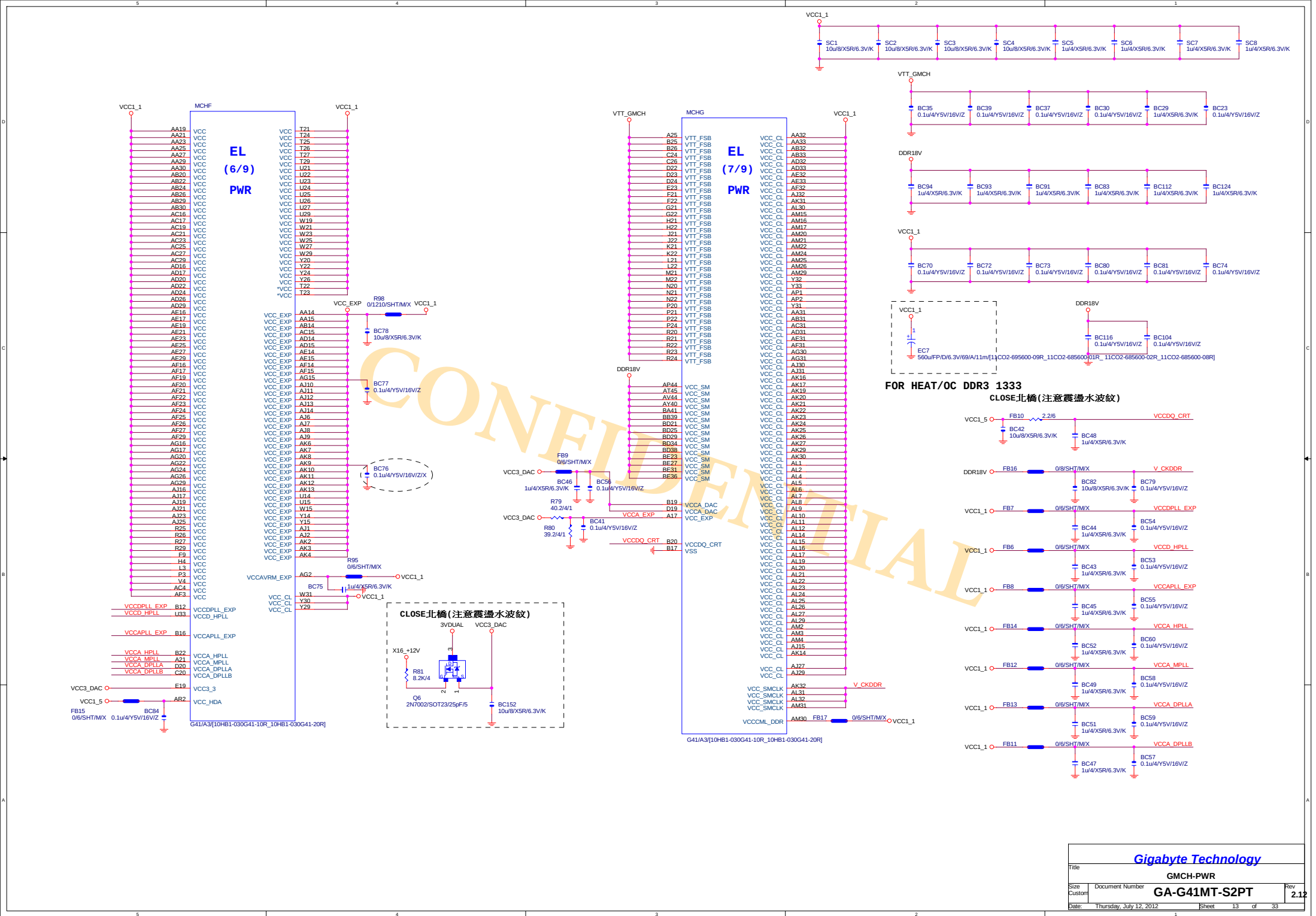
PECI:Platform Environment Control Interface

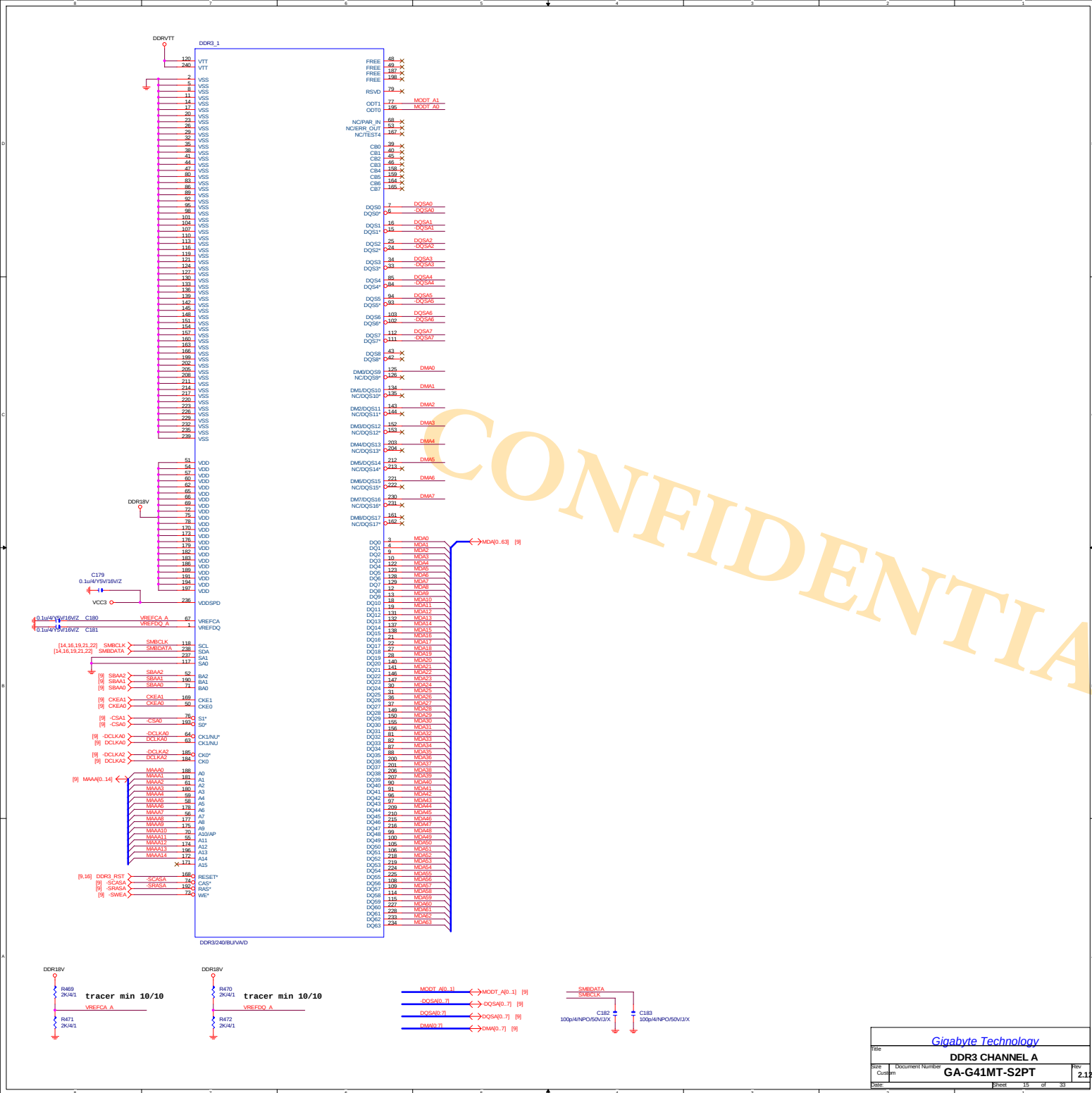






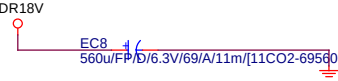




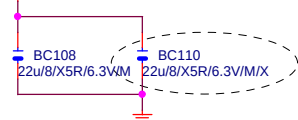


DDR TERMINATION
CHANNEL A

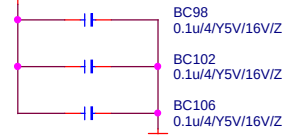
DDR18V Decouple



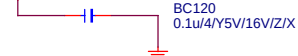
DDRVTT Decouple



DDR18V Decouple

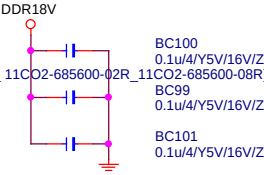


DDRVTT Decouple

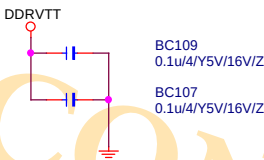


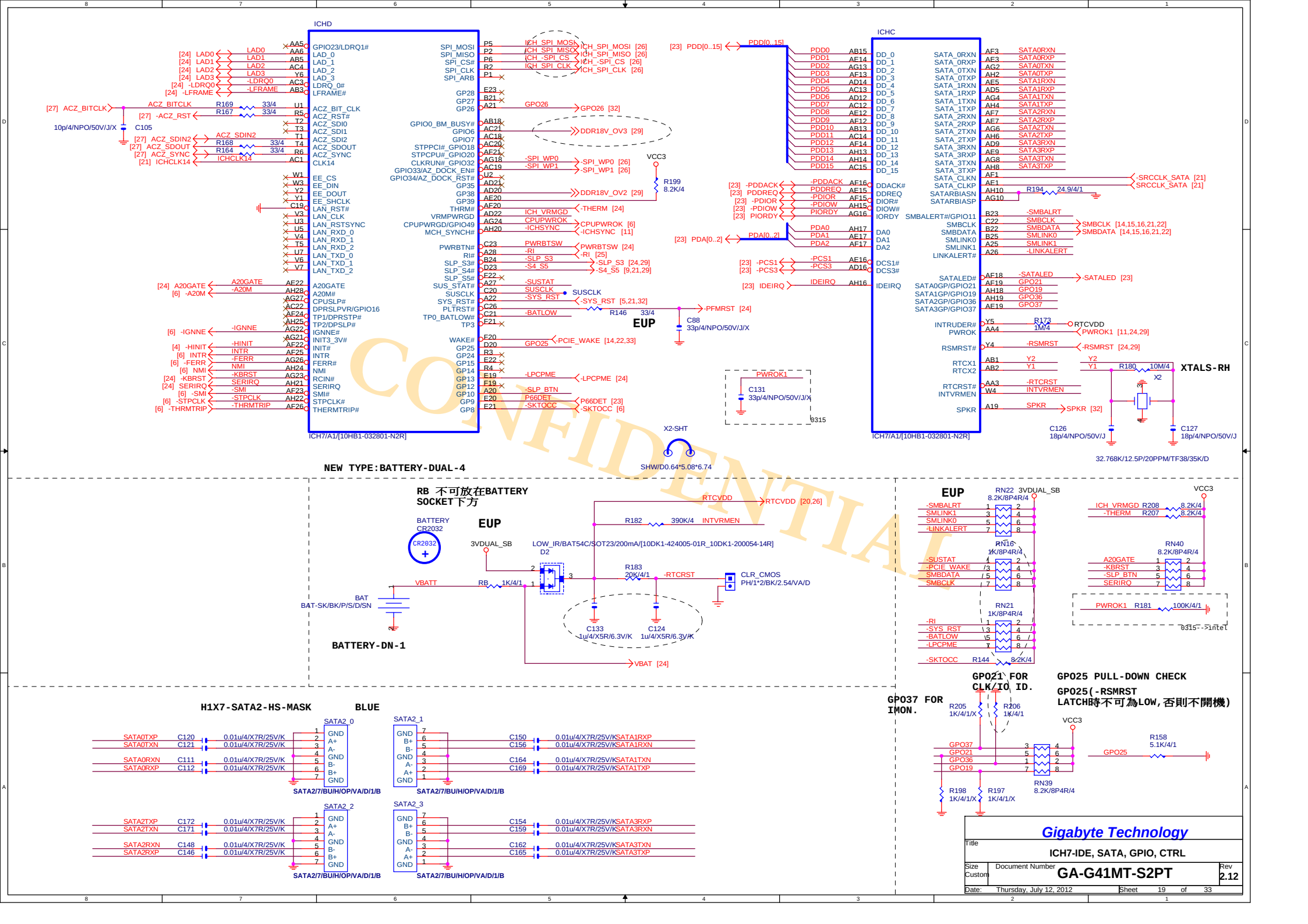
DDR TERMINATION
CHANNEL B

DDR18V Decouple

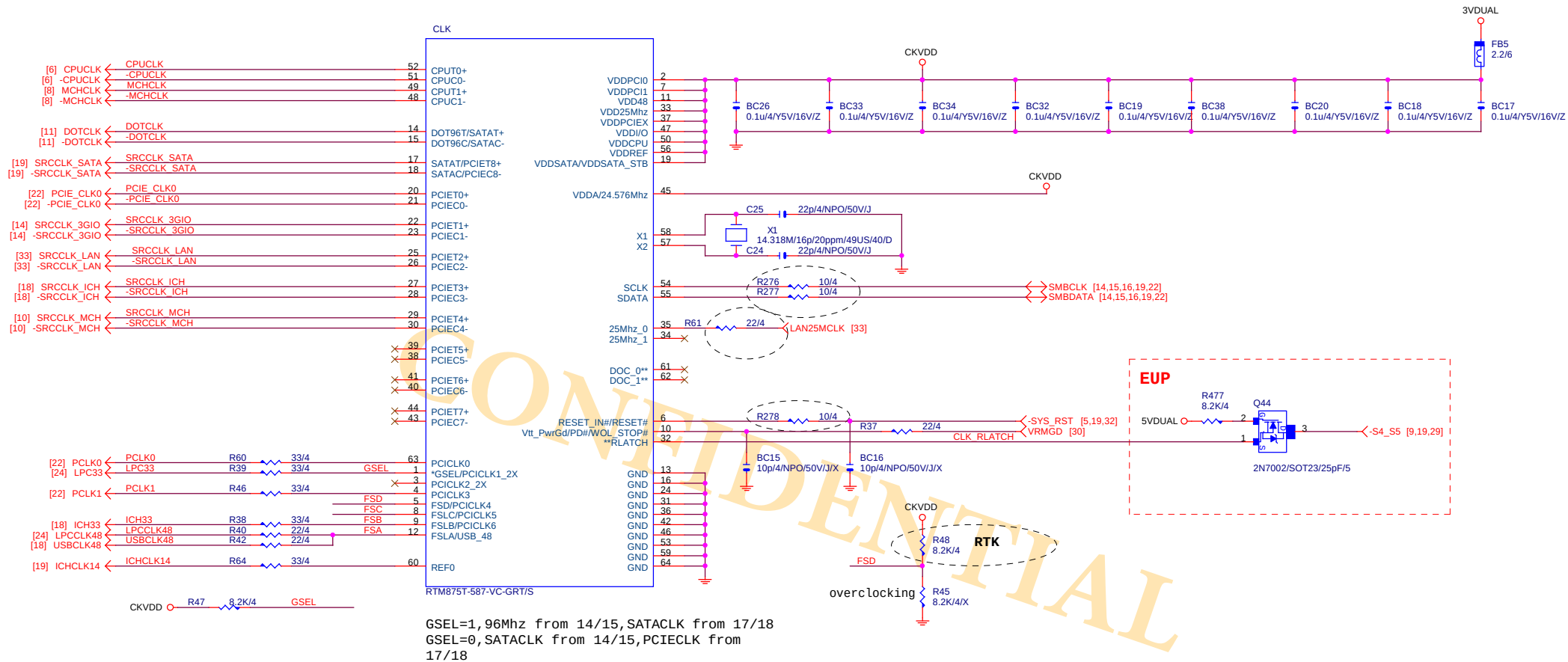


DDRVTT Decouple





CLK GEN CK505



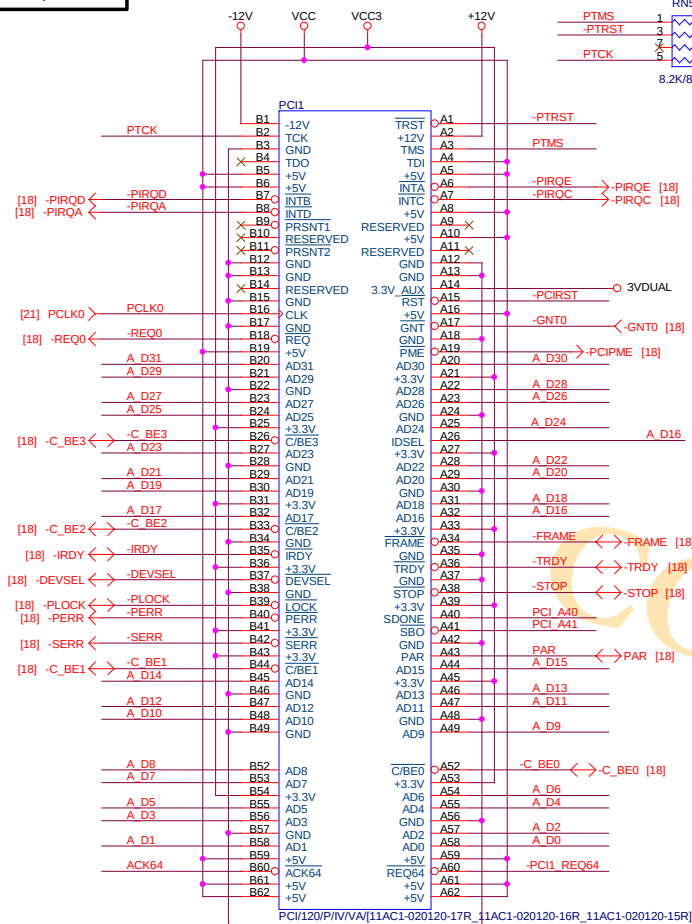
```
GSEL=1,96Mhz from 14/15,SATACLK from 17/18
GSEL=0,SATACLK from 14/15,PCIECLK from
17/18
```

[5] FSBSEL0 → R36 → 1K/4/1 FSA

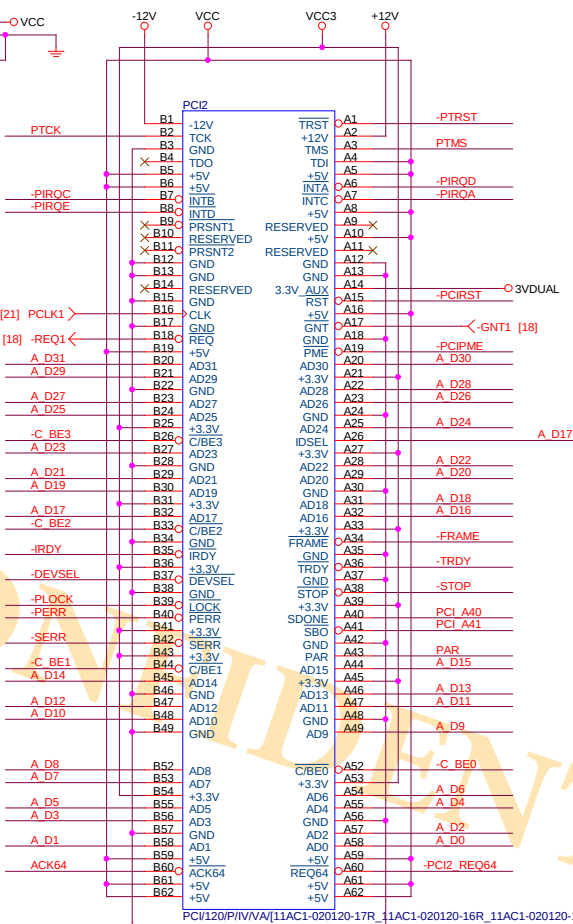
[5] FSBSEL1 → R43 → 1K/4/1 FSB

[5] FSBSEL2 → R44 → 1K/4/1 FSC

PCI1, 2 SLOT

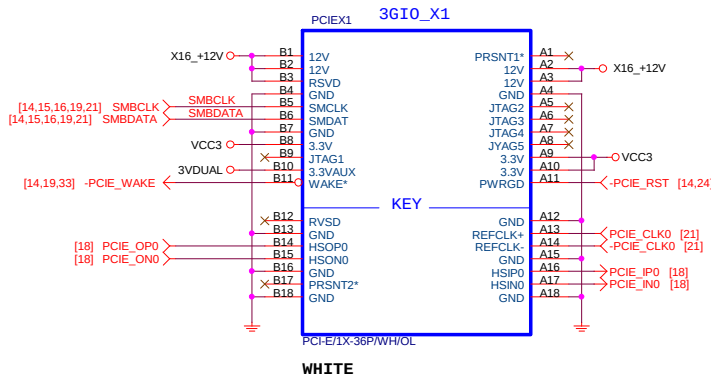


AD_16/-PIRQ(E-D-C-A)/-REQ0/-GNT0



AD_17/-PIRQ(D-C-A-E)/-REQ1/-GNT1

PCI-E*1



WHITE

[18] A_D[0..31] <-> A_D0..31

-PCIRST <-> PCIRST [18]

Place close to PCI1

[18] -REQ4 <-> REQ4
[18] -REQ3 <-> REQ3
[18] -REQ1 <-> REQ1
[18] -REQ2 <-> REQ2

[18] -REQ0 <-> REQ0

[18] -PCIRST <-> PCIRST [18]
[18] -REQ4 <-> REQ4
[18] -REQ3 <-> REQ3
[18] -REQ1 <-> REQ1
[18] -REQ2 <-> REQ2

[18] -REQ4 <-> REQ4
[18] -REQ3 <-> REQ3
[18] -REQ1 <-> REQ1
[18] -REQ2 <-> REQ2

[18] -REQ0 <-> REQ0

[18] -PCIRST <-> PCIRST [18]
[18] -REQ4 <-> REQ4
[18] -REQ3 <-> REQ3
[18] -REQ1 <-> REQ1
[18] -REQ2 <-> REQ2

[18] -PCIRST <-> PCIRST [18]
[18] -REQ4 <-> REQ4
[18] -REQ3 <-> REQ3
[18] -REQ1 <-> REQ1
[18] -REQ2 <-> REQ2

+12V
BC21
0.1u4/Y5V/16V/Z

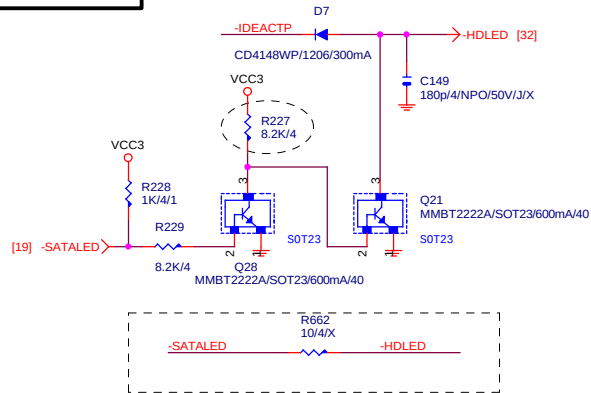
VCC
BC89
0.1u4/Y5V/16V/Z

VCC3
BC61 0.1u4/Y5V/16V/Z
BC63 0.1u4/Y5V/16V/Z/X
BC85 0.1u4/Y5V/16V/Z/X
EC4
560uF/P/D/6.3V/69A/11m[11CO2-695600-09R_11CO2-685600-01R_11CO2-685600-02R_11CO2-685600-08R]

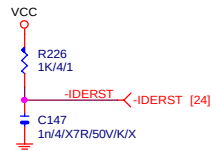
Gigabyte Technology

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IDE/SATA LED



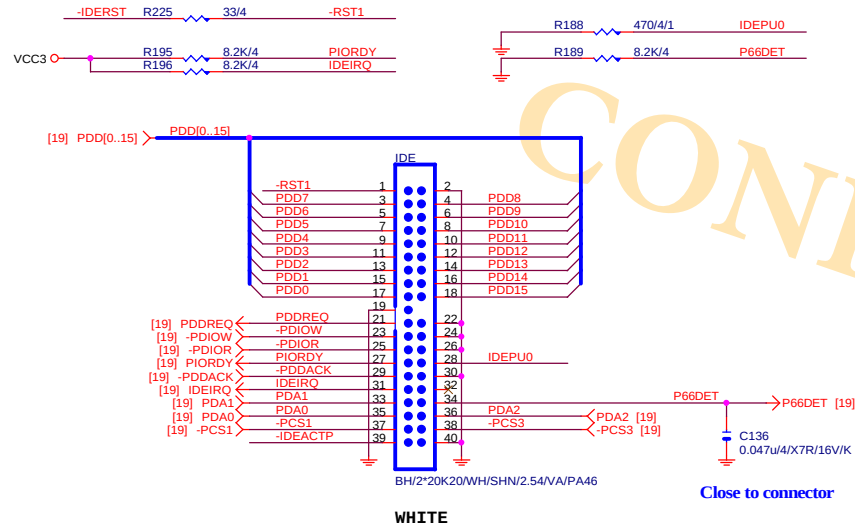
IDE RESET



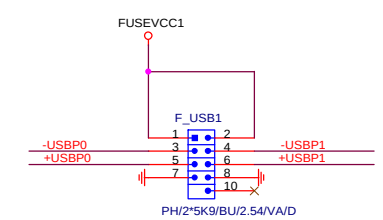
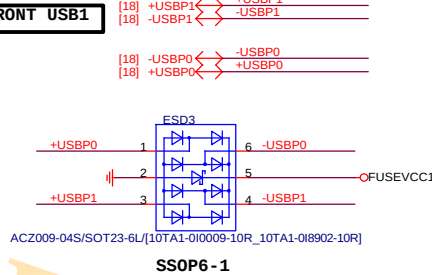
FLOPPY

N/A

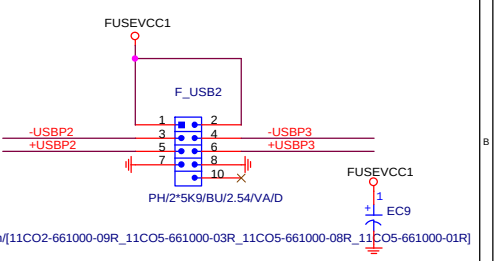
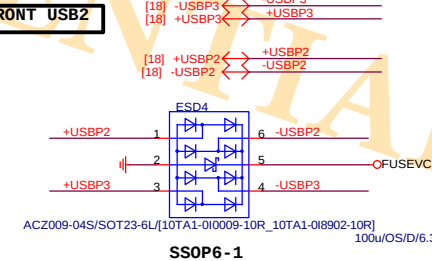
IDE



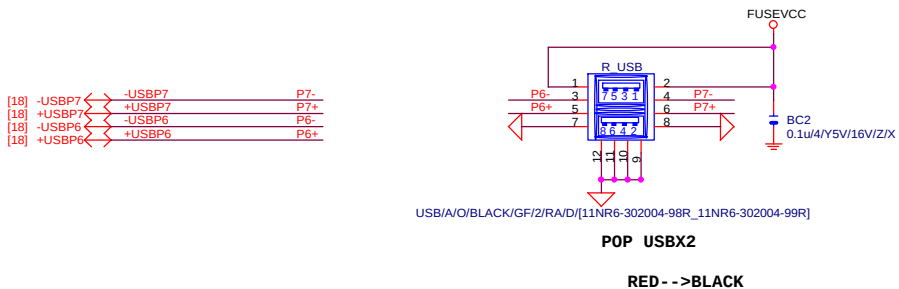
FRONT USB1



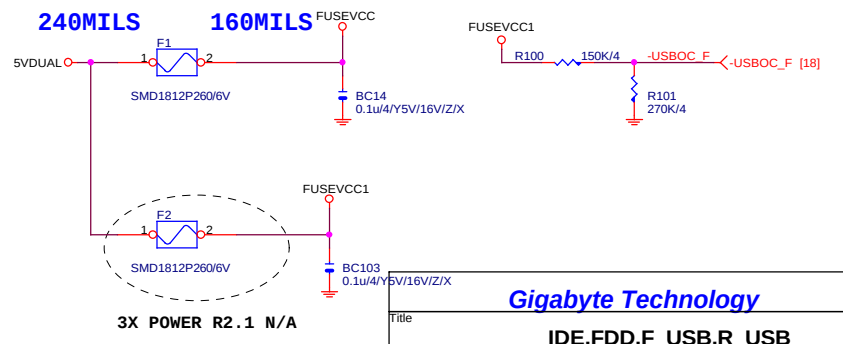
FRONT USB2



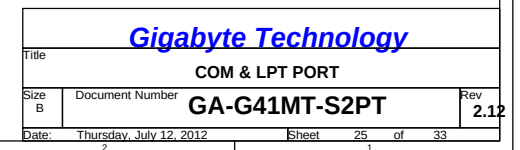
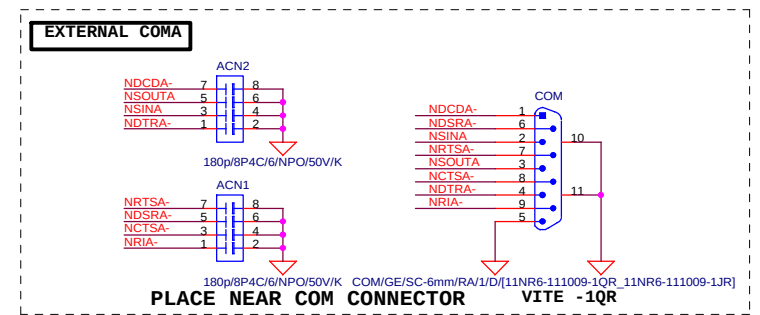
REAR USB



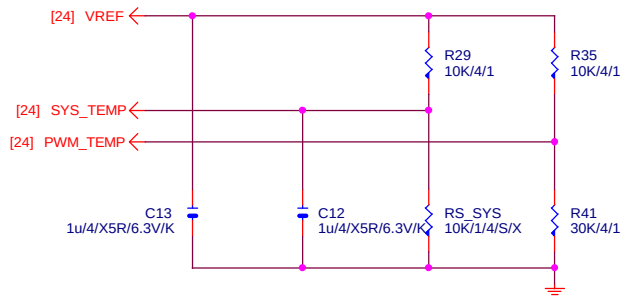
USB FUSE



Gigabyte Technology		
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IDE,FDD,F USB,R USB		
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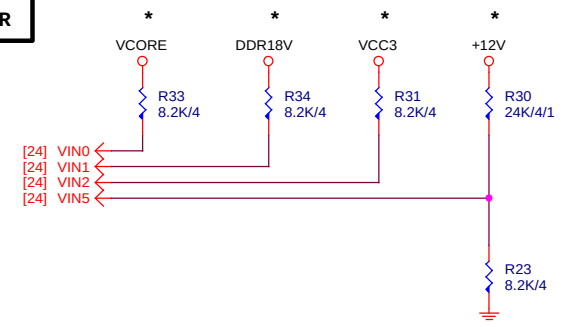


TEMP H/W MONITOR

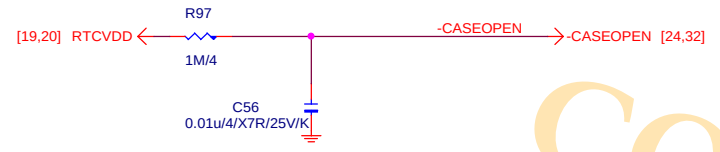


VOLTAGE-- H/W MONITOR

IT8720-->VIN5
IT8718-->VIN7



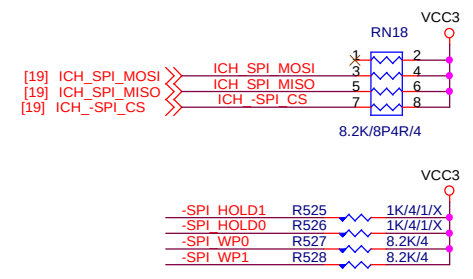
CASE OPEN



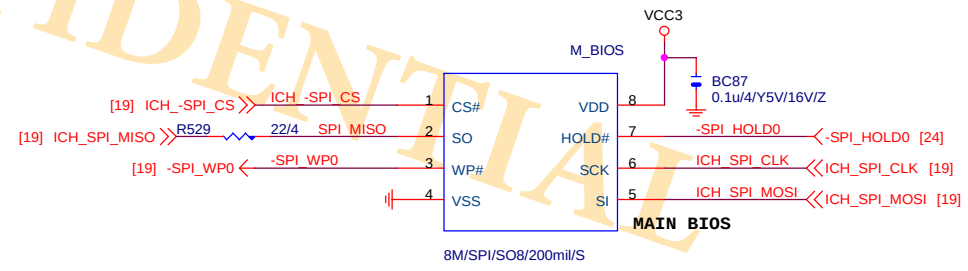
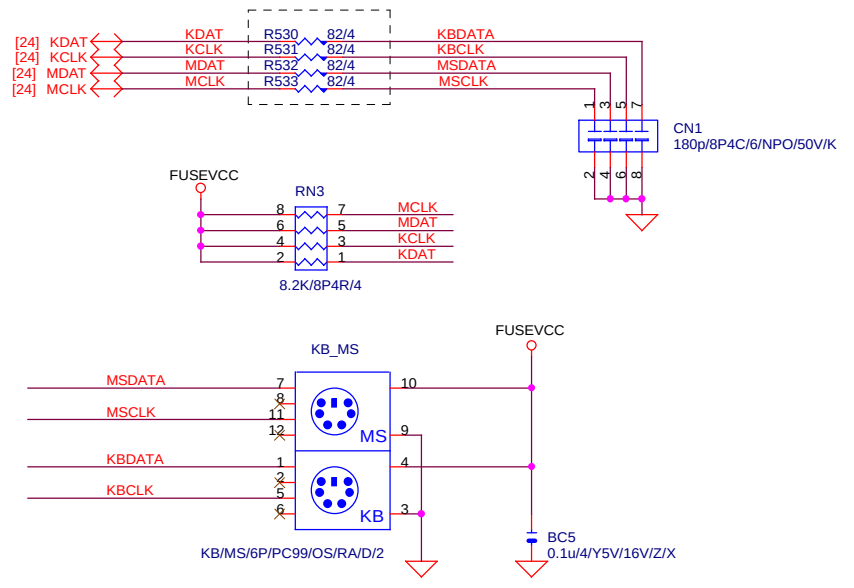
Case Open Circuits

DUAL BIOS

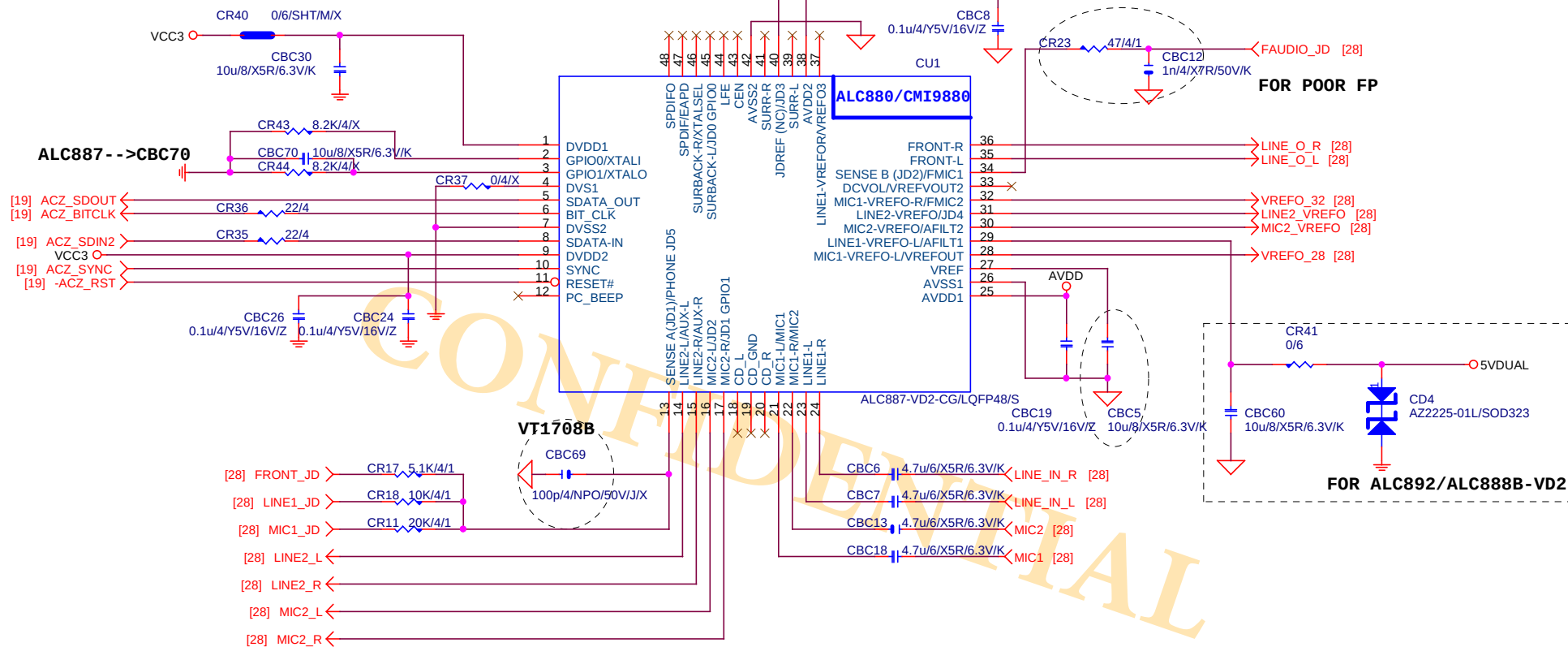
(NEW HOLD# DESIGN)



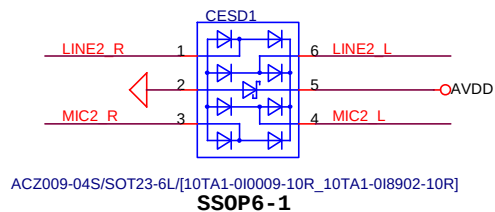
KB/MS



C0-LAY
ALC888B/ALC892R/ALC892/ALC887-VD/ALC889
VT1708S/VT2021

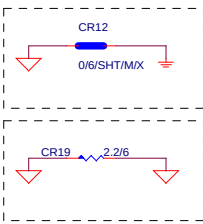
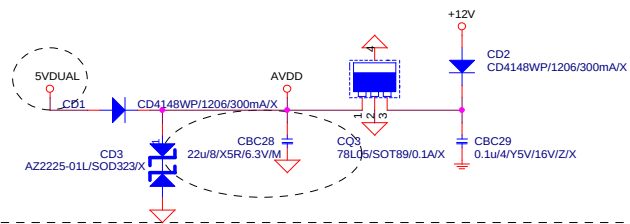


Can Support Amp Out



Gigabyte Technology			
Title			
AZALIA ALC887-VD2			
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CODEC POWER/EMI PAD



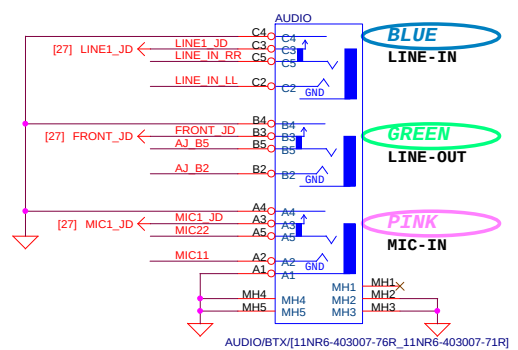
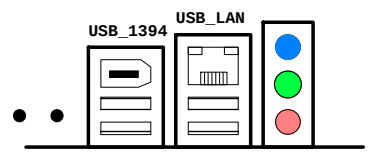
CD IN

N/A

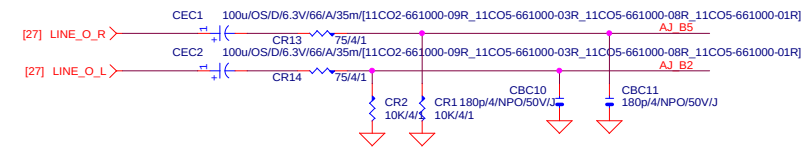
SPDIF IN

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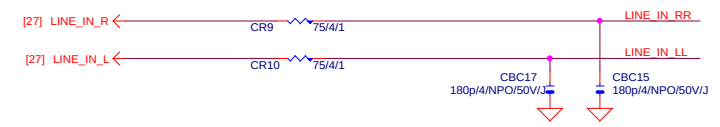
AZALIA JACK



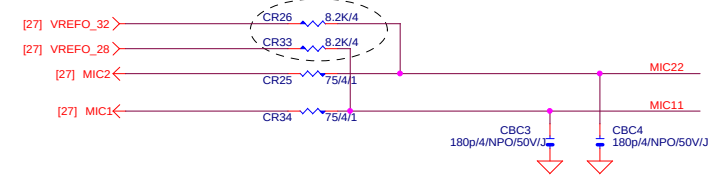
LINE-OUT



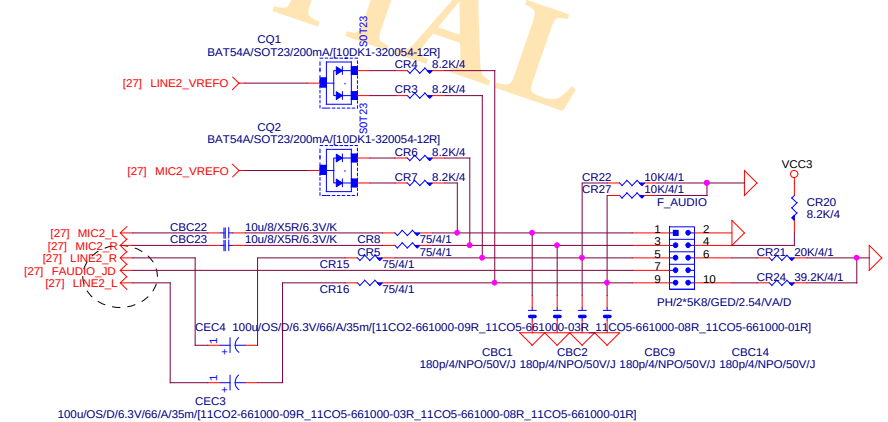
LINE-IN



MIC-IN

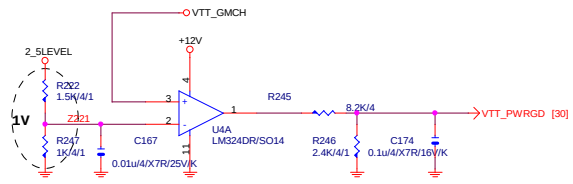


AZALIA FRONT PANEL

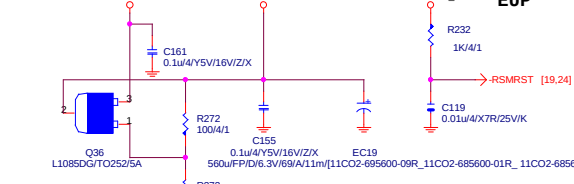


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AUDIO JACK			
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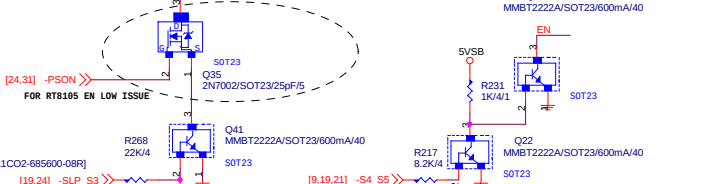
VTT_PWRGD



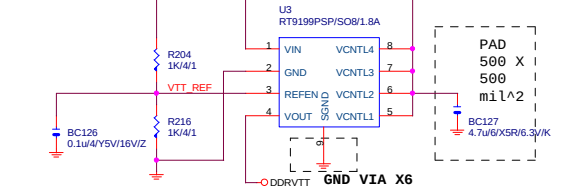
3VDUAL



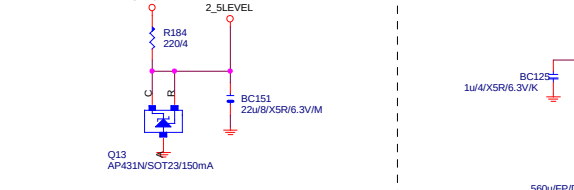
PWR_SEQ



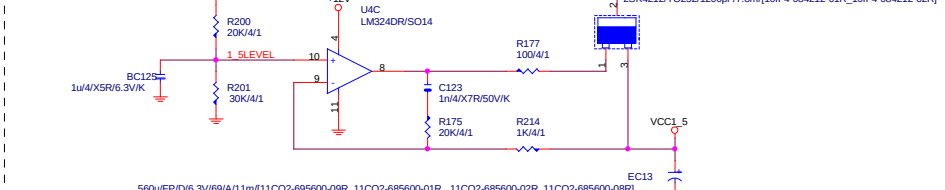
DDRVTT



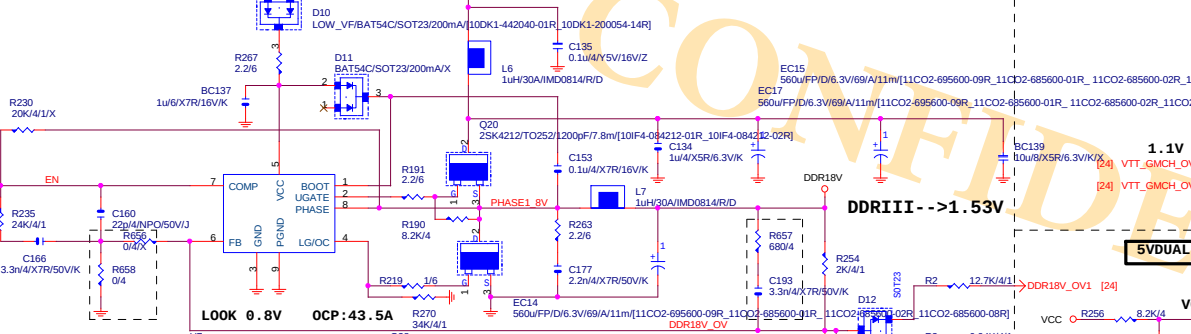
2_5LEVEL



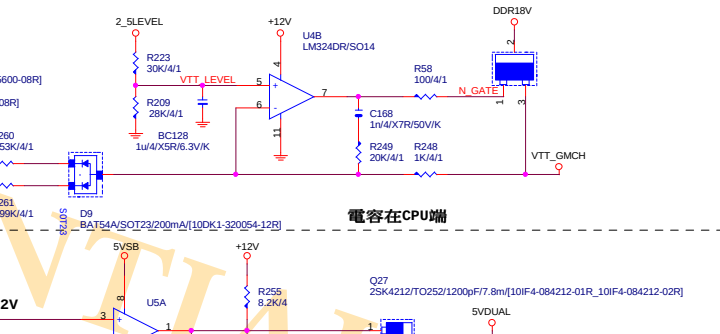
VCC1_5



DDR18V

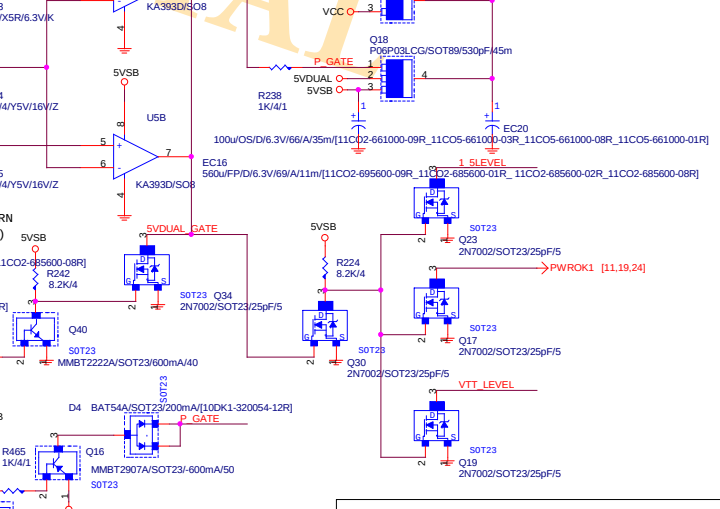
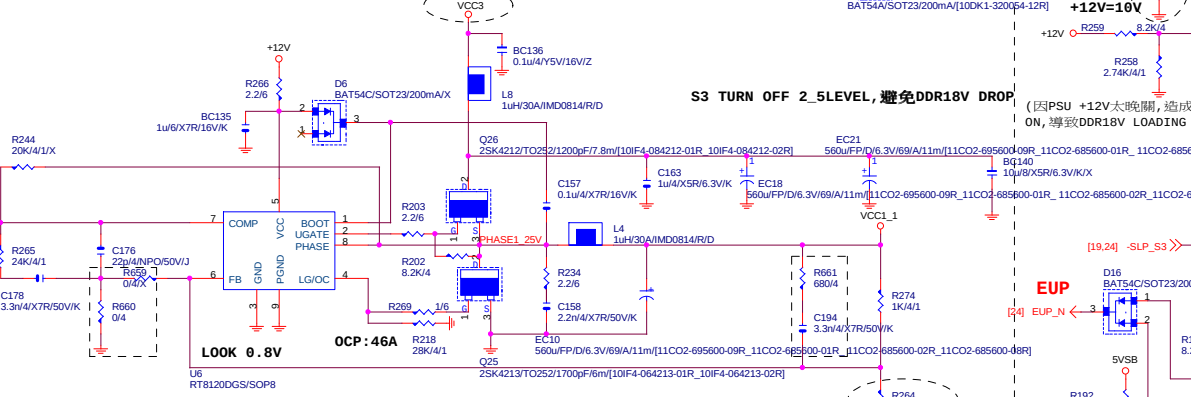


VTT_GMCH



Rocset=(Iocp*Lgate, rdson)/Iocset
Iocset=10uA
OCP: 43.5A(Rdson: 7.8m)-61.8A(Rdson: 5.5m)

VCC1_1

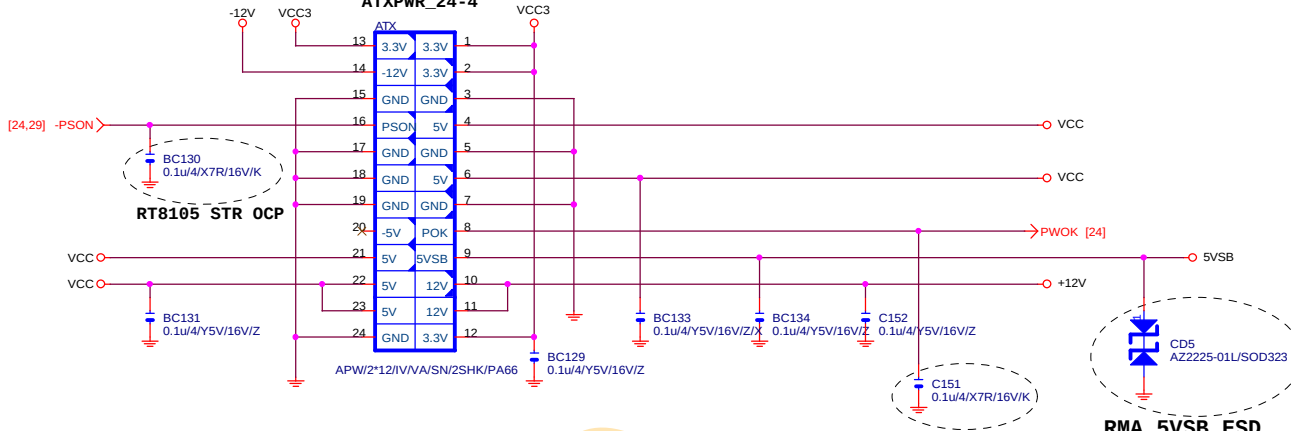


Rocset=(Iocp*Lgate, rdson)/Iocset
Iocset=10uA
OCP: 46A(Rdson: 6m)-67A(Rdson: 4.2m)

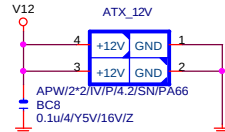
VCC1_1=1.23V
FOR HEAT/OC DDR3 1333

ATX

ATX POWER CONNECTOR



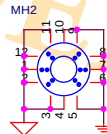
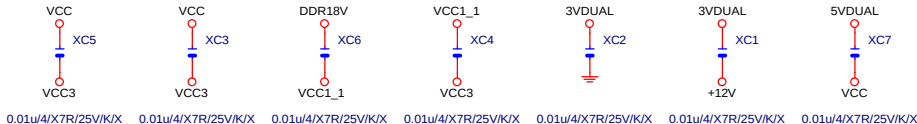
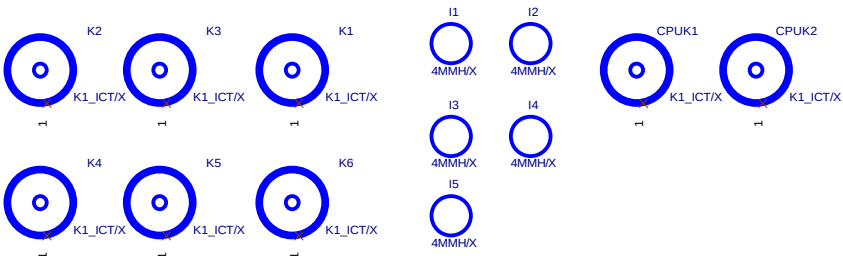
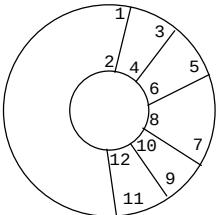
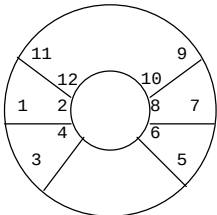
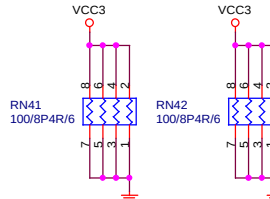
ATX_12V



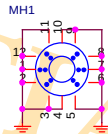
ATX_4-

[illegible]

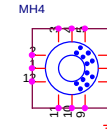
FIX PWR AcBel(ATX-400C-A2ADP



HOLE_4-RH-



HOLE_4-RH-5MM-



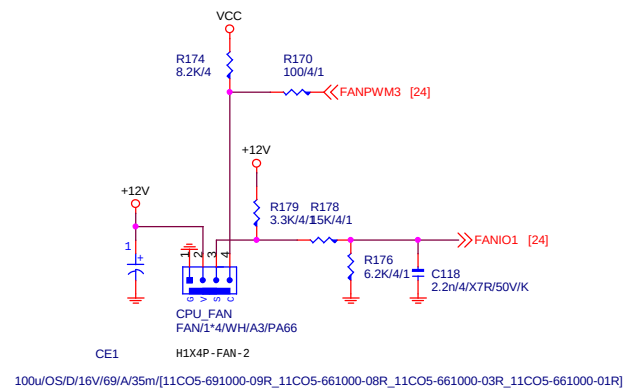
Gigabyte Technology

Title	ATX POWER CONNECTOR
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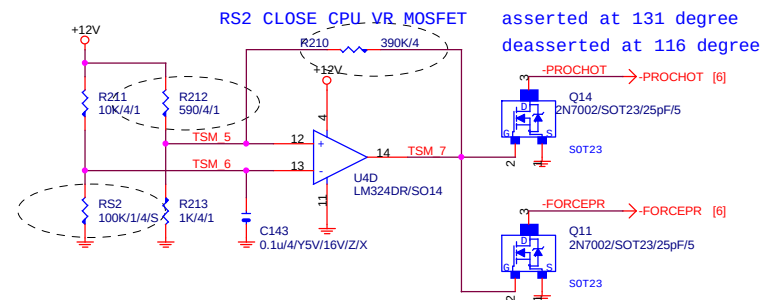
Size Custom	Document Number GA-G41MT-S2PT	Rev 2.1
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Date: Thursday, July 12, 2012	Sheet 31 of 33
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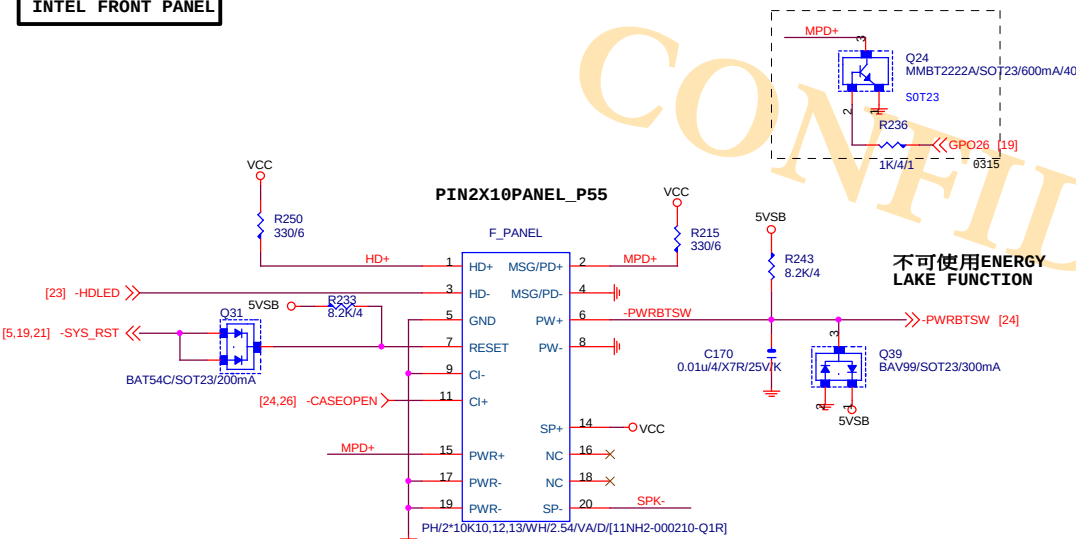
CPU SMART FAN SMART FAN



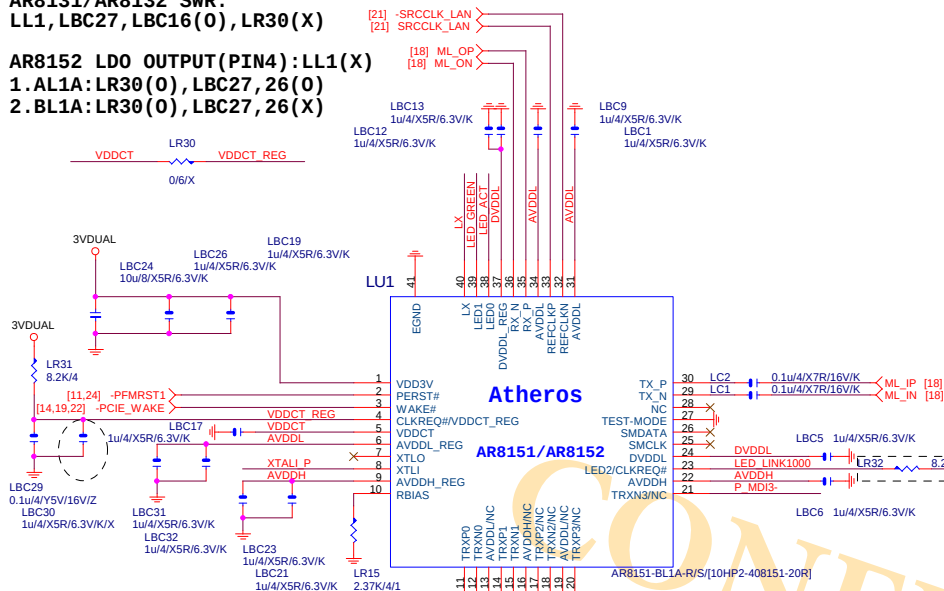
PROCESSOR HOT



INTEL FRONT PANEL



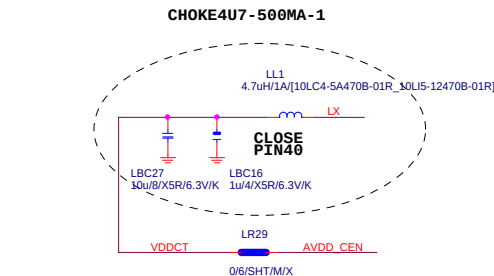
AR8152 LDO OUTPUT(PIN4):LL1(X)
1.AL1A:LR30(0),LBC27,26(0)
2.BL1A:LR30(0),LBC27,26(X)



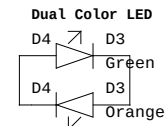
LC9
10p/4/NPO/50V/J

XTALI P

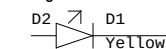
LAN25MCLK [21]



LED0 (ACT) LED1 (LINK) LED2 (LINK1G)

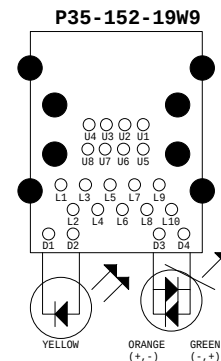
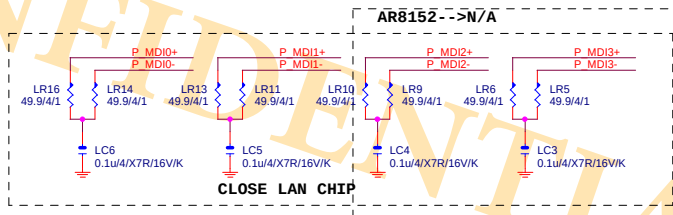


Single Color LED



CLKREO# PULL-UP FOR EFUSE:

1. AR8151: LR31(0), LR32(X)
2. AR8152: LR31(X), LR32(0)



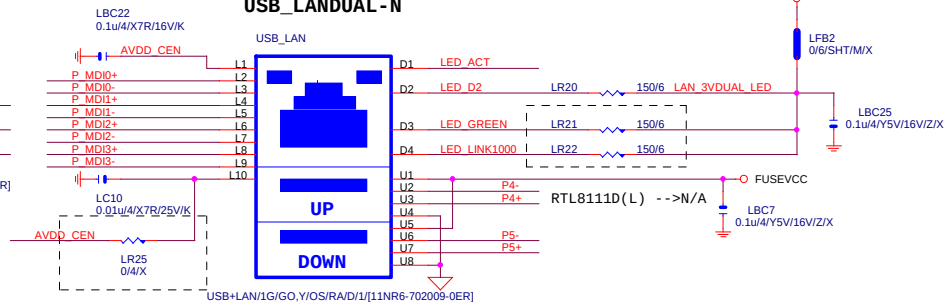
LED LINK1000 1 6 LED D2

2 5 LAN 3V DUAL LED

LED GREEN 3 4 LED ACT

ESD5

ACZ009-04S/SOT23-6L/10TA1-010009-10R 10TA1-018902-10R



EMI

LR33

0/6/SHT/M/X

